

FIG. 1

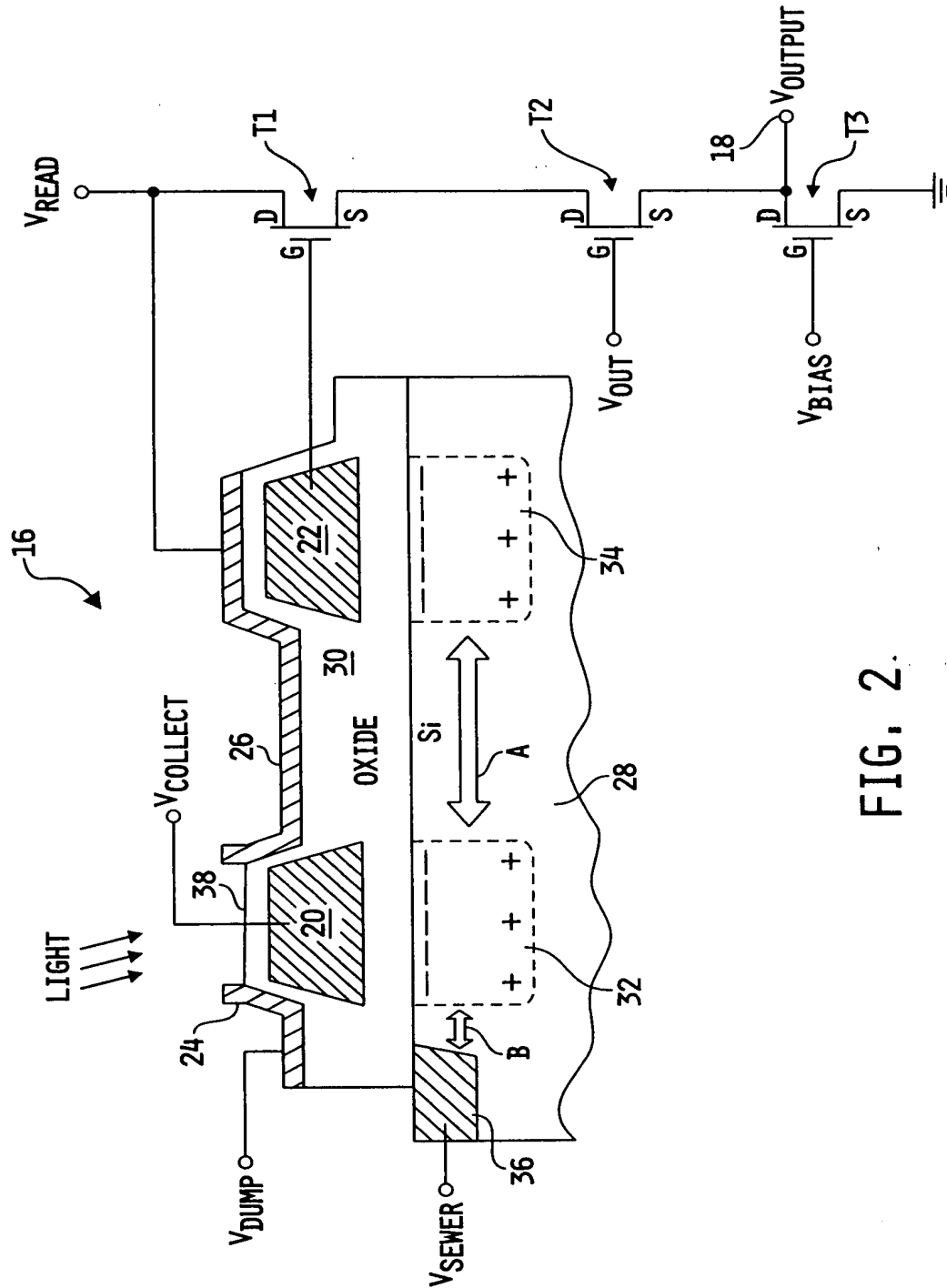


FIG. 2.

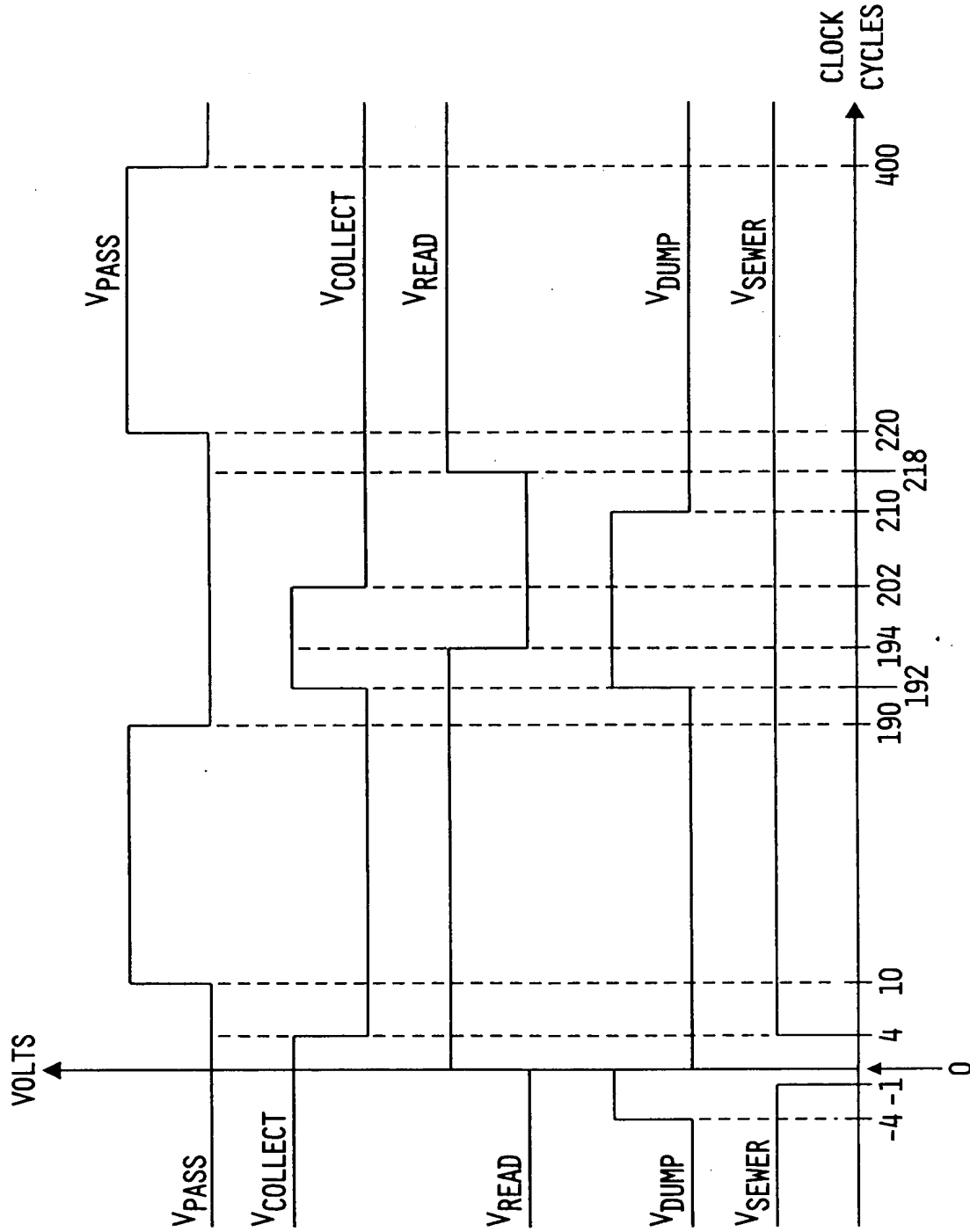


FIG. 3

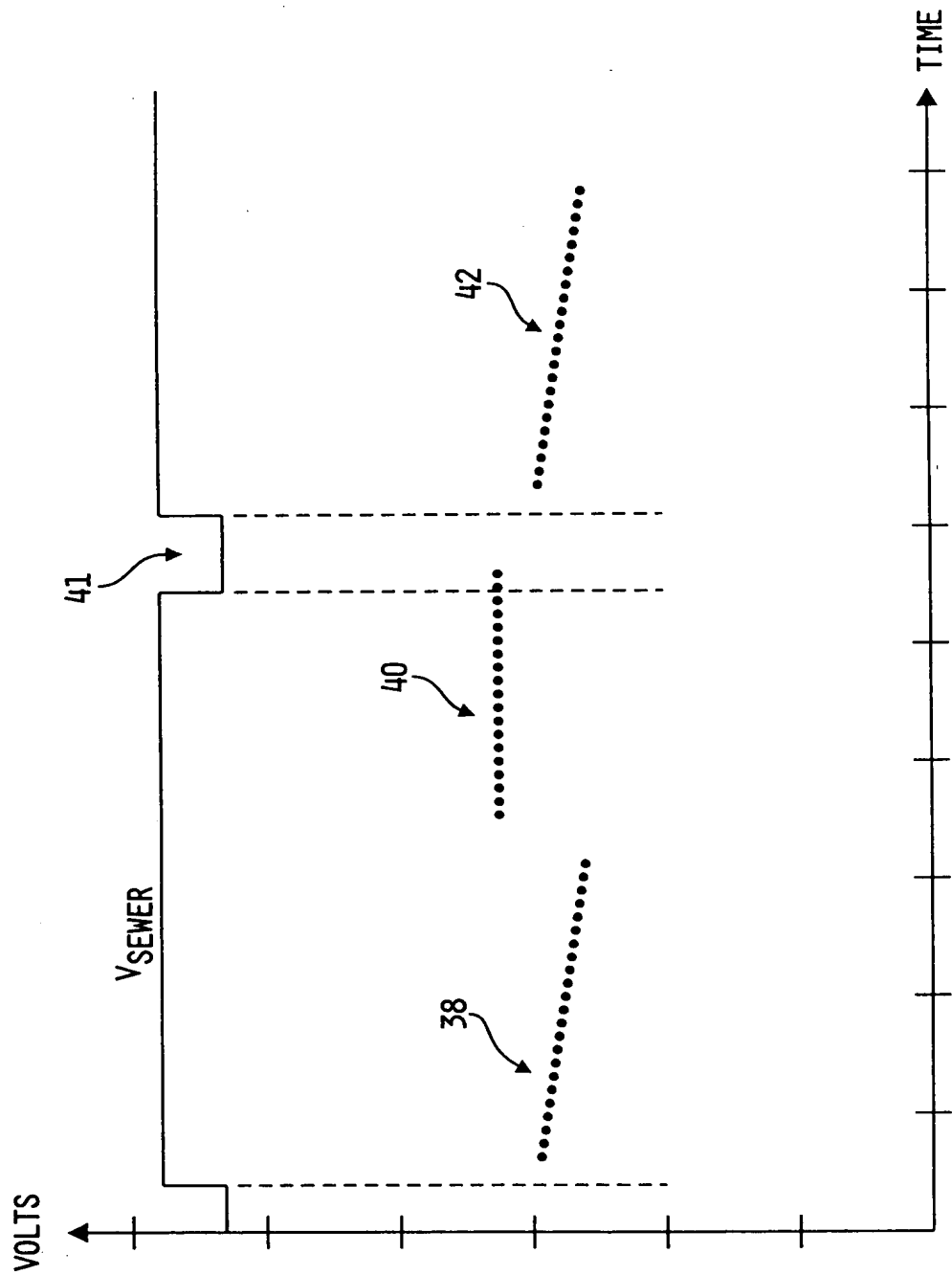


FIG. 4

TABLE 1

Open Circuit Faults	Effect at Output
VCC Open	Array cannot be changed
DUMP Open	Array cannot be changed
COLLECT Open	Cannot write a 0
READ Open	Stuck at 0
Power to READ Open	Stuck at 0
FLOAT to T1 Open	Stuck at 0
T1 source Open	Stuck at 0
T2 gate Open	Stuck at 0
T2 source Open	Stuck at 0
T2 drain Open	Stuck at 0

TABLE 2

Short Circuit Faults	Effect at Output
READ shorted to FLOAT	Always 1
READ shorted to COLLECT	No 0
DUMP shorted to COLLECT	Always 0
DUMP shorted to Active	Always 0
FLOAT shorted to substrate	Always 1
COLLECT shorted to substrate	Always 1
DUMP shorted to substrate	Settles to 0
READ shorted to substrate	Always 1
VCC(at n+) shorted to substrate	Always 1
T1 gate to drain shorted	Always 1
T1 gate to source shorted	Always 0
T1 gate to channel shorted	Always 1
T1 source to drain shorted	Always 1
T2 gate to drain shorted	Column read affected-no pixels readable
T2 gate to source shorted	Always 1
T2 gate to channel shorted	Always 1
T2 source to drain shorted	Column read affected-no pixels readable

FIG. 5